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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Brent Kelley et al.		
Title:	Processor Surrogate for use in Multiprocessor Systems and Multiprocessor System Using Same		
App. No.:	10/683,859	Filed:	10/10/2003
Examiner:	STIGLIC, Ryan M.	Group Art Unit:	2112
Atty. Dkt. No.:	TT5565	Confirmation No.:	1162

M/S RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned is providing the patents, publications, applications or other information identified in the attached:

☒ Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449
☐ Other: n/a

to the Examinr's attention in the above-identified application. Citation of such information shall not be construed as:

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2. a representation that a search has been made, other than as described below; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to the Commissioner for Patents on 07/14/2006.

Laura H. Andre
Typed or Printed Name

Signature

FEES DUE

This Information Disclosure Statement is being filed:

☐ within three months of the filing date of a national application or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.

☒ before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

☐ during the period specified in § 1.97(c). Each item of information contained in this Information Disclosure Statement was cited in a communication from a patent office in a foreign counterpart application not more than three months prior to the filing of this Information Disclosure Statement.

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The Commissioner is hereby authorized to charge any fees due, or refund any credit to Deposit Account 01-0365.

Respectfully submitted,

Date 7/14/06

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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1

	Complete if Known
Application Number	10/683,859
Filing Date	10/10/2003
First Named Inventor	Brent Kelley
Art Unit	2112
Examiner Name	STIGLIC, Ryan M.
Attorney Docket Number	TT5565

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	1-A	"Swift Interconnects Ripple the Multiprocessing Waters," Cost Journal, Online, XP002342675, January 2003, pp. 60-62	
	2-A	"The AMD Opteron Processor for Multiprocessor Servers," Chetana N. Keltcher et al., IEEE, 2003, pp. 66-76	
	3-A		
	4-A		
	5-A		
	6-A		
	7-A		
	8-A		
	9-A		
	10-A		

Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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